

Diagram illustrating the test fixture setup for a 16-bit parallel adder. The central component is the 16-BIT PARALLEL ADDER IC, which is connected to various test points and components.

Test Points and Components:

- POTENTIOMETER:** A vertical strip of potentiometers labeled KMD2, KMD1, W1A, SP, PO, L1A, WSPA, and PU, each connected to a specific pin of the IC.
- EXTENSION BOARD SOCKET:** A socket connected to the top of the IC.
- E-PROM SOCKET #1 to #5:** Five sockets labeled 1 System, 2 System, 3 System, 4, and 5, connected to the bottom of the IC.
- TERMINAL STRIP X1:** A horizontal strip of terminals labeled 40, 36, 32, 29, 25, 21, 18, 15, 12, 8, 5, and 1, connected to the bottom of the IC.

Handwritten Notes:

- GND Testpoint:** An arrow points to the GND pin of the IC.
- Length angle terminals:** A bracket indicates the length of the terminal strip.



3	1960	22.10.85	Fr	Datum	02.09.85	24 350 31 053 D	LIEBHERR	Längen- / Winkelgeber	LTM 1125/UDSSR		=	
2	1945	15.10.85	bma	Bearb.	Molz				+			
1	1941	10.10.85	bma	Gepr.	sc							
Zust.	Änderung	Datum	Name	Norm	Urspr.				Ers. f	Ers. d.	3340-941.01.00.001-00	Blatt 5